

#### **Product Datasheet - Technical Specifications**



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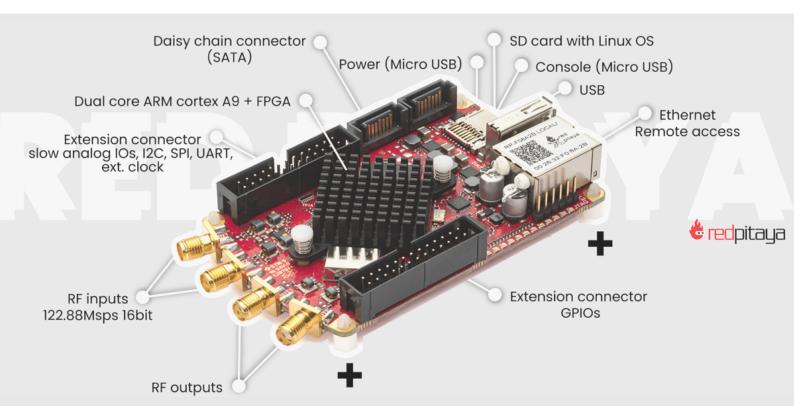
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# **SDRlab** 122-16

## SDRlab 122-16 was developed specifically for software-defined radio and more demanding RF applications.

It comes with two 16-bit 50-ohm inputs and 14-bit 50-ohm outputs, Xilinx Zynq 7020 FPGA for real-time processing capabilities plus an ultra-low phase noise 122.88MHz clock which makes it more hardware-compatible with HPSDR compliant applications. RF inputs are optimized for minimal distortion, noise and crosstalk which significantly improves reception and broadens the choice of antenna.



### Key features:

- Credit-card sized RF signal acquisition and generation platform
- Ethernet connectivity
- Xilinx SoC (CPU & FPGA)
- Two fast analog inputs and two outputs
- Possibility of integration into own system/product
- Open-source software code

- Works with Linux or Windows PC
- Free online apps (oscilloscope & signal generator, spectrum, logic analyzer, SDR)
- Can be controlled remotely using LabVIEW, MATLAB, Python, or Scilab
- Can be programmed to meet custom needs
- Supported by an app marketplace with several free apps available

# Technical specifications SDR<sup>lab</sup>122-16

BASIC			
Processor	DUAL CORE ARM CORTEX A9		
FPGA	FPGA Xilinx Zynq 7020 SOC		
RAM	512MB (4Gb)		
System memory	Micro SD up to 32GB		
Console connection	Micro USB		
Power connector	Micro USB		
Power consumption	5V, 2A max		
CONNECTIVITY			
Ethernet	1 Gbit		
USB	USB 2.0		
WIFI	Requires WIFI dongle		
RF input channels	2		
Sample rate	122.88 MS/s		
ADC resolution	16 bit		
Input impedance	50 Ohm		
Full scale voltage range	0.5Vpp/-2dBm		
Input coupling	AC		
Absolute max. input voltage range	DC max 50V (AC-coupled) 1Vpp for RF		
Bandwidth	300 kHz - 550 MHz		
RF OUTPUTS			
RF output channels	2		
Sample rate	122.88 MS/s		
DAC resolution	16 bit		
Load impedance	50 Ohm		
Voltage range	1Vpp/ +4 dBm		
Short circuit protection	N/A, RF transformer & AC-coupled		
Connector type	SMA		
Output slew rate	N/A		
Bandwidth	300 kHz - 60 MHz		
EXTENSION CONNECTOR			
Digital IOs	16		
Analog inputs	4		
Analog inputs voltage range	0-3,5V		
Sample rate	100kS/s		
Resolution	12bit		
Analog outputs	4		
Analog outputs voltage range	0-1,8V		
Communication interfaces	I2C, SPI, UART		
Available voltages	+5V,+3,3V,-4V		
External ADC clock	Yes		
SYNCHRONIZATION			
Trigger input	Through extension connector		
Daisy chain connection	Over SATA connection (up to 500 Mbps)		
Ref. clock input	N/A		